



**PATENT APPLICATION**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Confirmation Number: 6219

Hiroshi TAKENO

Group Art Unit: 1722

Application No.: 09/926,202

Examiner: G. Nagesh Rao

Filed: September 24, 2001

Attorney Dkt. No.: 107242-00024

For: MANUFACTURING PROCESS FOR SILICON EPITAXIAL WAFER

**PETITION FOR EXTENSION OF TIME**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Date: February 26, 2007

Sir:

Applicant petitions for an extension of time for three months for response to the outstanding Office Action dated September 15 2006 from December 15, 2006 to march 15, 2007.

Please find attached a check in the amount of \$1,020.00 to cover the fee discussed above. The Commissioner is hereby authorized to charge any fee deficiency or credit any overpayment associated with this communication to Deposit Account No. 01-2300, referencing our Docket Number 107242-00024.

Respectfully submitted,

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